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3IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of)
 HASEGAWA et al.)
 Application Number: 10/058,787)
 Filed: January 30, 2002)
 For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE,)
 METHOD OF TESTING SEMICONDUCTOR)
 INTEGRATED CIRCUIT DEVICE AND METHOD OF)
 MANUFACTURING SEMICONDUCTOR INTEGRATED)
 CIRCUIT DEVICE)



Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231

COVER LETTER

Sir:

[] The fee for submission of additional claims is calculated as shown below:

FOR	TOTAL WITH NEW CLAIMS ADDED	TOTAL CURRENTLY ON FILE	CLAIMS PAID	RATE	CALCULATION
Total Claims	12	0	-XX (Over 20)	x \$18	0
Independent Claims	3	0	1 (Over 3)	x \$84	0
MULTIPLE DEPENDENT CLAIM(S)	0	0		+ \$280	0
REDUCTION FOR FILING BY SMALL ENTITY (note 37 C.F.R. §§ 1.9, 1.27, 1.28). IF APPLICABLE, VERIFIED STATEMENT MUST BE ATTACHED				x ½	
			TOTAL		0.00

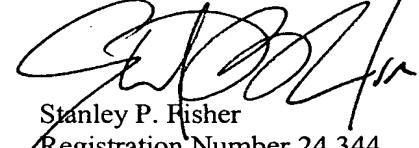
In addition, the below-identified communications are submitted in the above-captioned application or proceeding:

- | | | |
|-----|---|--|
| (X) | 12 pages of drawings (in English) | |
| () | Request for Priority | () Assignment Document |
| (X) | Response to Missing Parts
w/ English translation of Spec
and executed Declaration | () Petition under 37 C.F.R. § 1.47(a) |
| | | (X) Check for \$ 260.00 |



The Commissioner is hereby authorized to charge payment of any fees associated with this communication, including fees under 37 C.F.R. § 1.16 and 1.17 or credit any overpayment to **Deposit Account Number 08-1480**. A duplicate copy of this sheet is attached.

Respectfully submitted,



Stanley P. Fisher
Registration Number 24,344

JUAN CARLOS A. MARQUEZ
Registration No. 34,072

REED SMITH LLP
3110 Fairview Park Drive
Suite 1400
Falls Church, Virginia 22042
(703) 641-4200

April 5, 2002



UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS
 UNITED STATES PATENT AND TRADEMARK OFFICE
 WASHINGTON, D.C. 20231
www.uspto.gov

APPLICATION NUMBER	FILING/RECEIPT DATE	FIRST NAMED APPLICANT	ATTORNEY DOCKET NUMBER
10/058,787	01/30/2002	Masatoshi Hasegawa	ASAM.0038

CONFIRMATION NO. 4712

FORMALITIES LETTER



OC000000007679376

REED SMITH LLP
 Suite 1400
 3110 Fairview Park Drive
 Falls Church, VA 22042

Date Mailed: 03/20/2002



NOTICE TO FILE MISSING PARTS OF NONPROVISIONAL APPLICATION

FILED UNDER 37 CFR 1.53(b)

Filing Date Granted

An application number and filing date have been accorded to this application. The item(s) indicated below, however, are missing. Applicant is given **TWO MONTHS** from the date of this Notice within which to file all required items and pay any fees required below to avoid abandonment. Extensions of time may be obtained by filing a petition accompanied by the extension fee under the provisions of 37 CFR 1.136(a).

- The oath or declaration is unsigned.
- To avoid abandonment, a late filing fee or oath or declaration surcharge as set forth in 37 CFR 1.16(l) of \$130 for a non-small entity, must be submitted with the missing items identified in this letter.
- The application was filed in a language other than English. Applicant is required to provide an English translation of the specification and a statement that the translation is accurate. (See 37 CFR 1.52(d)).
- Applicant must file an English translation of the application, the \$ 130 fee set forth in 37 CFR 1.17(i), unless previously submitted, and a statement that the translation is accurate (37 CFR 1.52(d)).
- **The balance due by applicant is \$ 260.**
- Because your specification was filed in a language other than English, the Office was unable to determine the number of claims submitted. Additional claim fees may be due once the number of claims can be determined.

The application is informal since it does not comply with the regulations for the reason(s) indicated below.

The required item(s) identified below must be timely submitted to avoid abandonment:

- Substitute drawings in compliance with 37 CFR 1.84 because:
 - drawing sheets do not have the appropriate margin(s) (see 37 CFR 1.84(g)). Each sheet must include a top margin of at least 2.5 cm. (1 inch), a left side margin of at least 2.5 cm. (1 inch), a right side margin of at least 1.5 cm. (5/8 inch), and a bottom margin of at least 1.0 cm. (3/8 inch);
 - drawing figures contain text that is not in English (including, for example, a flow chart that was originally not in English that has been marked up to include the English text) see 37 CFR 1.84(p)(2) and 37 CFR 1.52(d)(1);

04/08/2002 SDEN0081 00000098 10058787
 01 FC105
 02 FC139

*A copy of this notice **MUST** be returned with the reply.*

J Robinson

Customer Service Center

Initial Patent Examination Division (703) 308-1202

PART 2 - COPY TO BE RETURNED WITH RESPONSE

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of
HASEGAWA et al.
Application Number: 10/058,787
Filed: January 30, 2002
**For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE,
METHOD OF TESTING SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE AND METHOD OF
MANUFACTURING SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE**



**Honorable Assistant Commissioner
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Washington, D.C. 20231**

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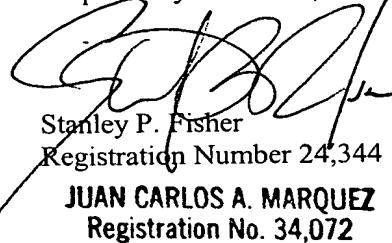
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w/ English translation of Spec
and executed Declaration (X) Check for \$ 260.00



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Respectfully submitted,



Stanley P. Fisher
Registration Number 24,344

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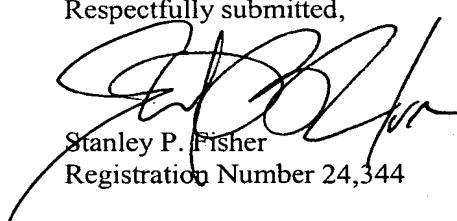
RESPONSE TO NOTICE TO FILE MISSING PARTS

Sir:

This is in response to the Notice of Missing Parts dated March 20, 2002, of the Initial Patent Examination Division of the Patent and Trademark Office, the period for response to which is set to expire on May 20, 2002. Submitted herewith is the verified English translation of the specification, the drawings for the above-captioned application, and the executed Declaration.

The fee of \$260.00 is enclosed. Please direct any inquiries in connection with this application to the undersigned.

Respectfully submitted,


Stanley P. Fisher
Registration Number 24,344

JUAN CARLOS A. MARQUEZ
Registration No. 34,072

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